REMARKS

Summary of Office Action

Claims 2, 3, and 18-22 stand rejected under 35 U.S.C. §103(a) as allegedly being unpatentable over allegedly admitted prior art (hereafter AAPA) of FIG. 1-3 in view of Nose et al. (US Pat. No. 6,819,311), previously cited.

Claims 5-17 stands allowed.

Summary of Amendment

Claim 18 has been amended. Claims 23 and 24 have been added. No new matter has been entered. Claims 2, 3 and 5-24 are currently pending for further consideration.

Allowed Claims

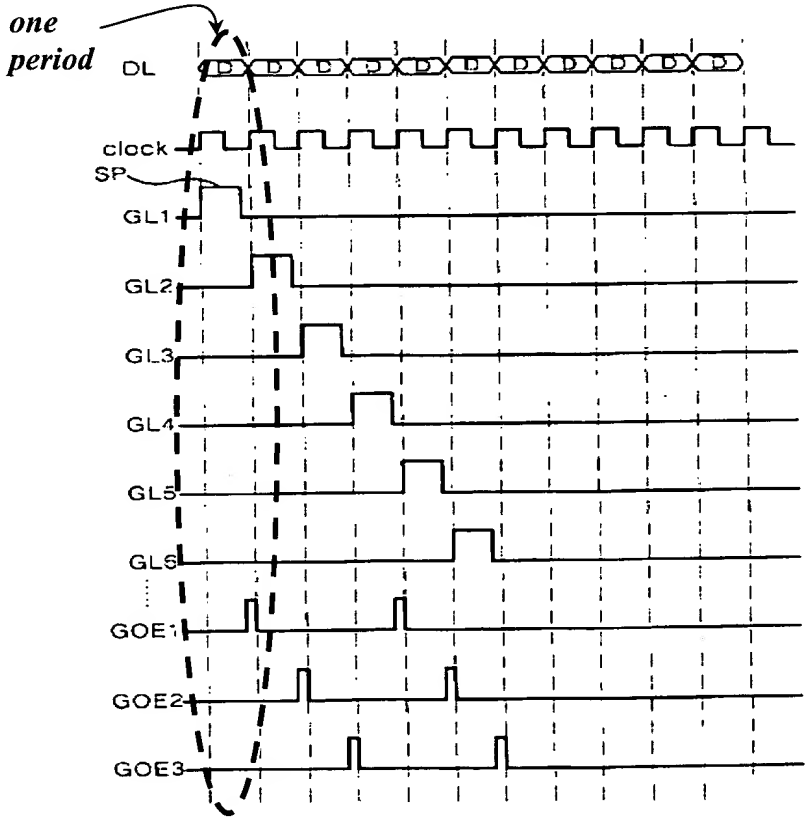
Applicants wish to thank the Examiner for allowance of claims 5-17. Applicants believe claims 2, 3, 18-24 are also allowable and therefore requests allowance of these claims for the reasons stated below

All Claims Comply With §103

Claims 2, 3, and 18-22 stand rejected under 35 U.S.C. §103(a) as allegedly being unpatentable over allegedly admitted prior art of FIGs. 1-3 of the present application in view of Nose et al., a reference cited in the previous Office Action. In particular, it appears that the new rejection in the outstanding Office Action is based on switching the primary and secondary references relied upon in the rejection from the previous Office Action. More specifically, the new rejection is now based on the related art FIGs. 1-3 of the present application as the primary teaching and Nose et al. as the secondary teaching. Applicants respectfully traverse.

In the new rejection, it is alleged that the related art FIG. 1-3 teaches all the limitations of independent claims 2 and 3 except for supplying the picture data to the data lines when the scanning pulse is applied to a first gate line of the two gate lines and supplying a black data to the data lines when the scanning pulse is applied to a second gate line of the two gate lines. (OA: page 2, last paragraph.) Applicants disagree.

Independent claims 2 and 3 recite, in part, "applying a scanning pulse to two gate lines during ***one period*** of the clock pulse." (Emphasis added.) As shown in related art FIG. 2 of the present application, reproduced below, a scanning pulse in the related art is applied to only one gate line during one period of the clock pulse as only one GOE signal is applied in one clock period.



Accordingly, Applicants respectfully submit that the allegedly admitted prior art does not teach such a feature as alleged in the rejection. Moreover, as explained in the previous response filed March 14, 2006 and incorporated herein by reference, Nose et al. teaches a completely different driving method. In particular, the related art method of FIGs. 1-3 is directed to sequentially selecting gate lines using three gate enable signals (i.e., GOE1-GOE2) that sequentially cycles through successive clock periods to prevent cross-talk effects. Nose et al., on the other hand, uses only one gate enable signal to select between two gate lines in a complementary fashion (i.e., one is off when the other is on) to reduce motion blur. (e.g., FIG. 12; Abstract). Accordingly, Applicants submit that one with ordinary skill in the art would not find any motivation to modify the related art method of FIGs. 1-3 with Nose et al.'s teachings as explained in the previous response. Accordingly, Applicants request that the §103 rejection of independent claims 2 and 3 be withdrawn.

Although Applicants believe that claim 18 is distinguishable over the alleged combination, in the interest of advancing prosecution, claim 18 has been amended to recite, in part, "selecting two gate lines that are separated by a predetermined number of gate lines based on received first to third gate output enable signals ***during one period of a clock pulse***." (Emphasis added.) Accordingly, the related art FIGs. 1-3 fail to teach or suggest such a feature as discussed above.

Moreover, although Applicants assert that the related art FIGs. 1-3 cannot be combined with Nose et al. for the reasons described above, even if, *in arguendo*, the teachings are combinable, both teachings fail to teach or suggest at least the step of "selecting ***two gate lines***

that are separated by a predetermined number of gate lines ***based on received first to third gate output enable signals*** during ***one period of a clock pulse***" as recited in claim 18, as amended. (Emphasis added.) Accordingly, the related art FIGs. 1-3 and Nose et al., whether taken individually or in combination, fail to teach all the features of claim 18 for at least this additional reason.

As claims 19-22 depend from claim 18, the related art FIGs. 1-3 and Nose et al., whether taken individually or in combination, fail to render claims 19-22 unpatentable for at least the reasons stated above. Accordingly, Applicants request that the §103 rejection of claims 19-22 be withdrawn.

New claims 23 and 24 depend from independent claims 2 and 3, respectively. Further, claims 23 and 24 recite that "applying the scanning pulse to two gate lines during one period of the clock pulse is based on the first to third gate output enable signals." None of the art of record, individually or in combination, teaches or suggests this claimed feature. Accordingly, claims 23 and 24 are allowable over the related art FIGs. 1-3 and Nose et al. for at least the reasons stated above.

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CONCLUSION

In view of the foregoing, reconsideration and timely allowance of the pending claims are respectfully requested. Should the Examiner feel that there are any issues outstanding after consideration of the response, the Examiner is invited to contact the Applicants' undersigned representative to expedite prosecution.

If there are any other fees due in connection with the filing of this response, please charge the fees to our Deposit Account No. 50-0310. If a fee is required for an extension of time under 37 C.F.R. 1.136 not accounted for above, such an extension is requested and the fee should also be charged to our Deposit Account.

Respectfully submitted,

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